AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A method of indicating reception performance of a wireless signal at ~~an electronic device~~ a radio frequency peripheral component card of a computer system, said method comprising the steps of:

a) receiving said wireless signal at said ~~electronic device~~ radio frequency peripheral component card;

b) demodulating said wireless signal;

c) determining an error rate of a digital data portion of said wireless signal; and

d) indicating a quality level of reception of said wireless signal at said ~~electronic device~~ radio frequency peripheral component card based on said error rate.

2. (Original) The method recited in Claim 1 wherein said quality level of reception is indicated via a light emitting device.

3. (Original) The method recited in Claim 1 further comprising the step of:

e) linearly translating said error rate into said quality level.

4. (Original) The method recited in Claim 1 wherein said error rate is a packet error rate.

5. (Original) The method recited in Claim 4 wherein said packet error rate is determined by a cyclic redundancy code (CRC) algorithm.

6. (Currently Amended) The method recited in Claim 4 wherein said packet error rate is determined by a forward error correction [[(FED)]] (FEC) algorithm.

7. (Original) The method recited in Claim 1 wherein said quality level is linearly proportional to said error rate of said wireless signal.

8. (Original) The method recited in Claim 1 further comprising the step of:

e) adaptively updating said step c) of determining said error rate and said step d) of indicating said quality level.

9. (Original) The method recited in Claim 1 further comprising the steps of:

e) recording a history of said quality level with respect to another variable;

f) identifying a maximum quality level; and

g) indicating when said quality signal is at said maximum level.

10. (Original) The method recited in Claim 1 further comprising the steps of:

h) providing feedback to control reception, said feedback related to said quality level of reception; and

i) adjusting said reception based on said feedback, thereby improving said quality level of said reception.

11. (Currently Amended) The ~~communication device~~ radio frequency peripheral component card of a computer system for receiving a wireless signal, said ~~communication device~~ radio frequency peripheral component card comprising:

a receiver;

a processor, said processor coupled to said receiver; and

a computer readable memory unit, said computer readable memory unit coupled to said processor, said computer readable memory unit containing program instructions stored therein that execute, via said processor, a method for providing a quality level of reception, said method comprising the steps of:

a) receiving said wireless signal at said ~~electronic device~~ radio frequency peripheral component card;

b) demodulating said wireless signal;

c) determining an error rate of a digital data portion of said wireless signal; and

d) indicating a quality level of reception of said wireless signal at said ~~electronic device~~ radio frequency peripheral component card based on said error rate.

12. (Currently Amended) The ~~communication device~~ radio frequency peripheral component card recited in Claim 11 wherein said quality level of reception is indicated via a light emitting device.

13. (Currently Amended) The ~~communication device~~ radio frequency peripheral component card recited in Claim 11 further comprising the step of:

e) linearly translating said error rate into said quality level.

14. (Currently Amended) The ~~communication device~~ radio frequency peripheral component card recited in Claim 11 wherein said error rate is a packet error rate.

15. (Currently Amended) The ~~communication device~~ radio frequency peripheral component card recited in Claim 14 wherein said packet error rate is determined by a cyclic redundancy code (CRC) algorithm.

16. (Currently Amended) (Currently Amended) The ~~communication device~~ radio frequency peripheral component card recited in Claim 14 wherein

said packet error rate is determined by a forward error correction [[(FED)]] (FEC) algorithm.

17. (Currently Amended) The ~~communication device~~ radio frequency peripheral component card recited in Claim 11 wherein said quality level is linearly proportional to said error rate of said wireless signal.

18. (Currently Amended) The ~~communication device~~ radio frequency peripheral component card recited in Claim 11 further comprising the step of:

e) adaptively updating said step c) of determining said error rate and said step d) of indicating said quality level.

19. (Currently Amended) The ~~communication device~~ radio frequency peripheral component card recited in Claim 11 further comprising the steps of:

e) recording a history of said quality level with respect to another variable;

f) identifying a maximum quality level; and

g) indicating when said quality signal is at said maximum level.

20. (Currently Amended) The ~~communication device~~ radio frequency peripheral component card recited in Claim 11 further comprising the steps of:

h) providing feedback to control reception, said feedback related to said quality level of reception; and

i) adjusting said reception based on said feedback, thereby improving said quality level of said reception.

21. (Currently Amended) A computer readable medium containing therein computer readable codes for causing ~~an electronic device~~ a radio frequency peripheral component card of a computer system to implement a method of managing [[said]] multipath signals, said method comprising the steps of:

a) receiving said wireless signal at said ~~electronic device~~ radio frequency peripheral component card;

b) demodulating said wireless signal;

c) determining an error rate of a digital data portion of said wireless signal; and

d) indicating a quality level of reception of said wireless signal at said ~~electronic device~~ radio frequency peripheral component card based on said error rate.

22. (Original) The computer readable medium recited in Claim 21 wherein said quality level of reception is indicated via a light emitting device.

23. (Original) The computer readable medium recited in Claim 21 further comprising the step of:

e) linearly translating said error rate into said quality level.

24. (Original) The computer readable medium recited in Claim 21 wherein said error rate is a packet error rate.

25. (Original) The computer readable medium recited in Claim 24 wherein said packet error rate is determined by a cyclic redundancy code (CRC) algorithm.

26. (Currently Amended) The computer readable medium recited in Claim 24 wherein said packet error rate is determined by a forward error correction [[(FED)]] (FEC) algorithm.

27. (Original) The computer readable medium recited in Claim 21 wherein said quality level is linearly proportional to said error rate of said wireless signal.

28. (Original) The computer readable medium recited in Claim 21 further comprising the step of:

e) adaptively updating said step c) of determining said error rate and said step d) of indicating said quality level.

29. (Original) The computer readable medium recited in Claim 21 further comprising the steps of:

e) recording a history of said quality level with respect to another variable;

f) identifying a maximum quality level; and

g) indicating when said quality signal is at said maximum level.

30. (Original) The computer readable medium recited in Claim 21 further comprising the steps of:

h) providing feedback to control reception, said feedback related to said quality level of reception; and

i) adjusting said reception based on said feedback, thereby improving said quality level of said reception.